# A Model Checking Intermediate Language Initial Proposal

#### The NSF:CCRI Team

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### Intermediate Language (IL) goals

The IL has been designed so that it

- is a general enough intermediate target language for MC
- can support a variety of user-facing modeling languages
- can be directly supported by tools or compiled to lower level languages
- can leverage SAT/SMT technology

IL models are meant to be produced and processed mostly by tools

- simple, easily parsable syntax;
- a rich set of data types
- little syntactic sugar, at least initially
- well-understood semantics
- a small but comprehensive set of commands.
- simple translations to lower level languages such as Btor2 and Alger

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# **Design principles — implications**

#### 1. Little direct support for many of the features offered by

- hardware modeling languages such as VHDL and Verilog or
- system modeling languages such as SMV, TLA+, PROMELA, UNITY, Lustre

However, enough capability to reduce problems in those languages to problems in the IL

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#### Extension the SMT-LIB language with new commands to define and check systems

- defines a transition system via the use of SMT formulas.
- generally imposes minimal syntactic restrictions on those formulas.
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- partitions state variables into input, output and local variables
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### Finite-state systems

#### but with an eye to infinite-state systems too

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# **Technical preliminaries**

Formally, a transition system is a pair S of predicates of the form

 $S = (I_{S}[i, o, s], T_{S}[i, o, s, i', o', s'])$ 

where

- *i* and *i* are two tuples of *input variables* with the same length and type
- o and o' are two tuples of output variables with the same length and type
- s and s' are two tuples of *local variables* with the same length and type
- Is, the initial state condition is a formula with free vars from [i, o, s]
- *T<sub>s</sub>*, the *transition condition* is a formula with free vars from [*i*, *o*, *s*, *i'*, *o'*, *s'*]

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### **SMT-LIB commands**

#### As in SMT-LIB

```
(set-logic L)
(declare-sort s n)
(define-sort s (u_1 \cdots u_n) \tau)
(declare-fun f ((x_1 \sigma_1) \cdots (x_n \sigma_n)) \sigma)
(define-fun f ((x_1 \sigma_1) \cdots (x_n \sigma_n)) \sigma t)
(declare-datatype d (···))
(assert F)
```

(perhaps a few more)

### **SMT-LIB commands**

#### New

(define-system S ····)

(check-system  $S \cdots$ )

#### (declare-enum-sort s ( $c_1 \cdots c_n$ ))

## Logical semantics

#### A **define-system** command implicitly defines a *model* (i.e., a Kripke structure) of First-Order Linear Temporal Logic (FO-LTL)

#### An FO-LTL formula *F*[*f*, *x*, *x'*] with

- free (immutable) constants/functions (aka, uninterpreted symbols) from f
- free (mutable) variables from x, x'

is satisfiable in an SMT theory  ${\mathcal T}$  if there is

- 1. a  $\mathcal{T}$ -interpretation  $\mathcal{I}$  of  $\mathbf{f}$  and
- 2. an infinite trace  $\pi$  over  $\mathbf{x}$  in  $\mathcal{I}$

that satisfy F

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### **Trace semantics**

Fix

- an FOL-LTL formula F[f, x, x'] over a theory T
- a  $\mathcal{T}$ -interpretation  $\mathcal{I}$  of **f**
- an infinite trace  $\pi = s_0, s_1, \ldots$  where  $s_i$  is an assignment of x into  $\mathcal{I}$  for all  $i \ge 0$

#### Let $\pi^i = s_i, s_{i+1}, \dots$ for all $i \ge 0$

 $(\mathcal{I},\pi)$  satisfies F , written  $(\mathcal{I},\pi)\models$  F, iff

- 1.  $\mathcal{I}[\mathbf{x} \mapsto s_0(\mathbf{x}), \mathbf{x}' \mapsto s_1(\mathbf{x})]$  satisfies F
- 2.  $(\mathcal{I}, \pi) \not\models G$
- 3.  $(\mathcal{I}, \pi) \models G_j$  for j = 1, 2
- 4.  $(\mathcal{I}, \pi^1) \models G$
- 5.  $(\mathcal{I}, \pi^i) \models G$  for all  $i = 0, \ldots,$

6. 
$$(\mathcal{I}, \pi^i) \models G$$
 for some  $i = 0, \dots,$   
7. ...

when F is atomic when F is  $\neg G$ when F is  $G_1 \land G_2$ when F is next G when F is always G when F is eventually

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```
Let \pi^i = s_i, s_{i+1}, \ldots for all i \ge 0
```

 $\begin{array}{lll} (\mathcal{I},\pi) & \textit{satisfies } F \text{ , written } (\mathcal{I},\pi) \models F \text{, iff} \\ \mathbf{1.} & \mathcal{I}[\mathbf{x} \mapsto s_0(\mathbf{x}), \mathbf{x}' \mapsto s_1(\mathbf{x})] \text{ satisfies } F & \text{when } F \text{ is atomic} \\ \mathbf{2.} & (\mathcal{I},\pi) \not\models G & \text{when } F \text{ is } \neg G \\ \mathbf{3.} & (\mathcal{I},\pi) \models G_j \text{ for } j = 1, 2 & \text{when } F \text{ is } G_1 \wedge G_2 \\ \mathbf{4.} & (\mathcal{I},\pi^1) \models G & \text{when } F \text{ is next } G \\ \mathbf{5.} & (\mathcal{I},\pi^i) \models G \text{ for all } i = 0, \dots, & \text{when } F \text{ is always } G \\ \mathbf{6.} & (\mathcal{I},\pi^i) \models G \text{ for some } i = 0, \dots, & \text{when } F \text{ is eventually } G \\ \mathbf{7.} & \cdots \end{array}$ 

### **Finite-Trace semantics**

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Let  $\pi^i = s_i, s_{i+1}, \ldots$  for all  $i \ge 0$ 

 $(\mathcal{I}, \pi)$  *n*-satisfies *F* for some n > 0, written  $(\mathcal{I}, \pi) \models_n F$ , iff

1.  $\mathcal{I}[\mathbf{x} \mapsto s_0(\mathbf{x}), \mathbf{x}' \mapsto s_1(\mathbf{x})]$  satisfies F when F is atomic 2.  $(\mathcal{I}, \pi) \not\models_n G$  when F is  $\neg G$ 3.  $(\mathcal{I}, \pi) \models_n G_j$  for j = 1, 2 when F is  $G_1 \wedge G_2$ 4.  $(\mathcal{I}, \pi^1) \models_{n-1} G$  and n - 1 > 0 when F is next G5.  $(\mathcal{I}, \pi^i) \models_{n-i} G$  for all  $i = 0, \dots, n - 1$  when F is always G6.  $(\mathcal{I}, \pi^i) \models_{n-i} G$  for some  $i = 0, \dots, n - 1$  when F is eventually G7. ...

# System definition command

```
(define-system S
 :input ((i_1 \ \delta_1) \ \cdots \ (i_m \ \delta_m)); input vars
 :output ((o_1 \ \tau_1) \ \cdots \ (o_n \ \tau_n)) ; output vars
 :local ((s_1 \sigma_1) \cdots (s_p \delta_p)); local vars
 :init
 :trans T
             P
 : inv
```

### (Base case)

- : initial state formula
- : transition formula
- : invariant formula

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#### where

- each var gets a primed copy:  $i'_1, \ldots, o'_1, \ldots, s'_1, \ldots$
- / and P are one-state formulas (over unprimed vars only)
- *T* is a two-state formula (over unprimed and primed vars)
- all attributes are optional and their order is immaterial
  - however, :input, :output, :local must occur before :init, :trans, :inv

# System definition command



(Base case)

- : initial state formula
- : transition formula
- : invariant formula

#### Semantics

$$S = (I_S, T_S) = (I[i, o, s], P[i, o, s] \land T[i, o, s, i', o', s'])$$

where  $i = (i_1, \ldots, i_m), o = (o_1, \ldots, o_n), s = (s_1, \ldots, s_n)$ 

S denotes the set of all infinite traces that satisfy the FO-LTL formula

 $I_{\rm s} \wedge \text{always} T_{\rm s}$
## System definition command

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 :output ((o_1 \ \tau_1) \ \cdots \ (o_n \ \tau_n)); output vars
 :local ((s_1 \sigma_1) \cdots (s_p \delta_p)); local vars
 :init /
 :trans T
 :inv
            P
```

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### Note:

Systems are meant to be *progressive*: every reachable state has a successor wrt  $T_s$ However, they may not be because of the generality of T and P(It is possible to define deadlocking systems)

### Default values for missing attributes

attribute	default
:input	()
:output	()
:local	()
:init	true
:trans	true
:inv	true

```
; The output of Delay is initially in [0,10] and
; then is the previous input
(define-system Delay
  :input ((in Int))
  :output ((out Int))
  :init (<= 0 out 10) ; more than one possible initial output
  :trans (= out' in) ; the new output is the old input
)
```

```
; A clocked lossless channel, stuttering when clock is false
(define-system StutteringClockedCopy
    :input ((clock Bool) (in Int))
```

```
:output ((out Int))
```

```
:init (=> clock (= out in)) ; out is arbitrary when clock is false
:trans (ite clock (= out' in') (= out' out))
```

(declare-datatype Event (par (X) (Absent) (Present (val X))))

; An event-triggered channel that arbitrarily loses its input data (define-system LossyIntChannel :input ((in (Event Int))) :output ((out (Event Int)))

```
:inv (or (= out in) (= out Absent))
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:input ((in (Event Int)))
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:inv (or (= out in) (= out Absent))
```

```
; Equivalent formulation using unconstrained local state (define-system LossvIntChannel
```

```
:input ((in (Event Int)))
:output ((out (Event Int)))
:local ((s Bool))
; at all times, whether the input event is transmitted
; or not depends on value of s
:inv (= out (ite s in Absent))
```

TimedSwitch models a timed light switch where, once on, the light stays on for 10 steps unless it is switched off before

A Boolean input is provided as a toggle signal

```
(define-enum-sort LightStatus (On Off))
```

```
: Guarded-transitions-style definition
(define-system TimedSwitch :input ((press Bool)) :output ((siq Bool))
 :local ((s LightStatus) (n Int))
 :inv (= sig (= s On))
 :init (and
   (= n \ 0)
   (ite press (= s \ On) (= s \ Off))
 :trans (and
   (=> (and (= s Off) (not press')) ; Off ->
       (and (= s' Off) (= n' n))) ; Off
   (=> (and (= s Off) press') : Off ->
       (and (= s' 0n) (= n' n))) : 0n
   (=> (and (= s On) (not press') (< 10 n)) ; On ->
       (and (= s' 0n) (= n' (+ n 1)))) : On
   (=> (and (= s On) (or press' (>= n 10)) ; On ->
       (and (= s' 0ff) (= n' 0))) : Off
```

```
(define-enum-sort LightStatus (On Off))
```

```
: Set-of-transitions-style definition
(define-system TimedSwitch2 :input ((press Bool)) :output ((sig Bool))
  :local ((s LightStatus) (n Int))
  :inv (= sig (= s On))
  :init (and
   (= n \ 0)
   (ite press (= s \ On) (= s \ Off))
  :trans
    (let (: Transitions
          (stav-off (and (= s Off) (not press') (= s' Off) (= n' n)))
          (turn-on (and (= s Off) press' (= s' On) (= n' n)))
          (stay-on (and (= s On) (not press') (< n 10) (= s' On)
                     (= n' (+ n 1)))
          (turn-off (and (= s 0n) (or press' (>= n 10)))
                      (= s' 0ff) (= n' 0)))
      (or stav-off turn-on turn-off stav-on)
```

```
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```

```
: Equational-style definition
(define-system TimedSwitch3 :input ((press Bool)) :output ((sig Bool))
  :local ((s LightStatus) (n Int))
 :inv (= sig (= s On))
  :init (and
   (= n 0)
    (= s (ite press On Off))
  :trans (and
    (= s' (ite press' (flip s)
            (ite (or (= s Off) (>= n 10)) Off
              On)))
    (= n' (ite (or (= s Off) (s' Off)) 0
           (+ n 1)))
```

(define-fun flip ((s LightStatus)) LightStatus (ite (= s Off) On Off))

### Special predicate: OnlyChange

For every system  $S = (I_S[i, o, s], T_S[i, o, s, i', o', s'])$ 

OnlyChange is a *multi-arity* predicate over **o**, **s**, **o'**, **s'**:

$$\mathsf{OnlyChange}(x_1,\ldots,x_n) \equiv \bigwedge \{y'=y \mid y \in (\boldsymbol{o} \cup \boldsymbol{s} \cup \boldsymbol{o'} \cup \boldsymbol{s'}) \setminus \{x_1,\ldots,x_n\}\}$$

### Fixes the value of all output and local variables not in $(x_1, \ldots, x_n)$

It is a useful abbreviation in transition conditions to express transitions that leave many state variables unchanged

**Note:** OnlyChange $(x_1, \ldots, x_n)$  does not actually constrain the  $x_i$ 's in any way

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```
; increment n_i iff n = i; n_i is 0 initially if not incremented
(define-system Increment :input ((i Int))
  :output ((inc Bool) (n1 Int) (n2 Int) ··· (n5 Int))
  :inv (= inc (<= 1 i 5))
  :init (and
   (=> (= n 1) (and (= n1 1) (= n2 n3 n4 n5 0)))
    (=> (= n 5) (and (= n5 1) (= n1 n2 n3 n4 0)))
    (=> (not (<= 1 n 5)) (= n1 n2 n3 n4 n5 0))
  :trans (and
    (=> (= n' 1) (and (= n1' (+ n1 1)) (OnlyChange inc n1)))
    (=> (= n' 5) (and (= n5' (+ n5 1)) (OnlyChange inc n5)))
    (=> (not (<= 1 n' 5)) (OnlyChange inc))
```

## System definition — Synchronous composition



#### where

- **1.** q > 0 and each  $S_i$  is the name of a system other than S
- **2.**  $S_1, \ldots, S_q$  need not be all distinct
- 3. each  $N_i$  is a local synonym for  $S_i$ , with  $N_1, \ldots N_q$  distinct
- 4. each  $x_i$  consists of S's variables of the same type as  $S_i$ 's input
- 5. each  $y_i$  consists of S's local/output variables of the same type as  $S_i$ 's output
- 6. the directed subsystem graph rooted at *S* is acyclic

### System definition — Synchronous composition



#### Semantics

Let  $S_k = (I_k[i_k, o_k, s_k], T_k[i_k, o_k, s_k, i'_k, o'_k, s'_k])$  for k = 1, ..., q, with  $s_1, ..., s_q$  all distinct Let  $i = (i_1, ..., i_m)$ ,  $o = (o_1, ..., o_n)$ ,  $s = s_1, ..., s_q$ ,  $s_1, ..., s_q$   $S = (I_S[i, o, s], T_S[i, o, s, i', o', s'])$ with  $I_S = \bigwedge_{k=1}^q I_k[x_k, y_k, s_k]$   $T_S = \bigwedge_{k=1}^q T_k[x_k, y_k, s_k, x'_k, y'_k, s'_k]$ 

### System definition — Synchr. composition extended



- : component subsystem
- ; component subsystem
  - : initial state formula
  - : transition formula
  - : invariant formula

### **Semantics**

$$S = (I_{S}[i, o, s], T_{S}[i, o, s, i', o', s'])$$

with  $I_{s} = I \wedge \bigwedge_{k=1}^{q} I_{k}[\mathbf{x}_{k}, \mathbf{y}_{k}, \mathbf{s}_{k}]$   $T_{s} = T \wedge P \wedge \bigwedge_{k=1}^{q} T_{k}[\mathbf{x}_{k}, \mathbf{y}_{k}, \mathbf{s}_{k}, \mathbf{x}_{k}', \mathbf{y}_{k}', \mathbf{s}_{k}']$ 

```
(define-system Latch :input ((s Bool) (r Bool)) :output ((o Bool))
 :local ((b Bool))
 :trans (= o' (or (and s (or (not r) b))
                   (and (not s) (not r) o)))
(define-system OneBitCounter :input ((inc Bool) (start Bool))
 :output ((out Bool) (carry Bool))
 :local ((set Bool) (reset Bool))
 :subsvs (L (Latch set reset out))
 :inv (and (= set (and inc (not reset)))
            (= reset (or carry start))
            (= carry (and inc out)))
(define-system ThreeBitCounter
 :input ((inc Bool) (start Bool))
 :output ((out0 Bool) (out1 Bool) (out2 Bool))
 :local ((car0 Bool) (car1 Bool) (car2 Bool))
 :subsys (C1 (OneBitCounter inc start out0 car0))
 :subsvs (C2 (OneBitCounter car0 start out1 car1))
 :subsys (C3 (OneBitCounter car1 start out2 car2))
```

### Expressiveness

**define-system** + SMT-LIB commands and types appear sufficient to allow faithful reductions from (full or large fragment of)

- Moore and Mealy machines
- I/O automata
- SMV and nuXMV
- UNITY
- TLA+
- Reactive Modules
- Lustre
- SAL

## System checking command

check-system	S
:input	$((i_1 \ \delta_1) \ \cdots \ (i_m \ \delta_m))$
:output	$( (o_1 \ \tau_1) \ \cdots \ (o_n \ \tau_n) )$
:local	$((s_1 \sigma_1) \cdots (s_p \delta_p))$
:assumption	(a A)
:reachable	(r R)
:fairness	(f F)
:current	(c C)
:query	$(q (g_1 \cdots g_q))$

- ; renaming of S's input vars
- ; renaming of S's output vars
- ; renaming of S's local vars
- ; environmental assumption
- ; reachability condition
- ; fairness condition
- ; initiality condition
- ; trace query to be checked

## System checking command

(check-system	S		
:input	$(i_1 \ \delta_1) \ \cdots \ (i_m \ \delta_m))$	;	r
:output	$((o_1 \ \tau_1) \ \cdots \ (o_n \ \tau_n))$	;	r
:local	$((s_1 \sigma_1) \cdots (s_p \delta_p))$	;	r
:assumption	(a A)	;	е
:reachable	(r R)	;	r
:fairness	(f F)	;	f
:current	(c C)	;	i
:query	$(q (g_1 \cdots g_q))$	;	t

- ; renaming of S's input vars ; renaming of S's output vars ; renaming of S's local vars ; environmental assumption ; reachability condition ; fairness condition ; initiality condition
- ; trace query to be checked

#### where

- a, r, f, c, q are identifiers; each  $g_i$  ranges over  $\{a, r, f, c\}$
- *C* is a one-state (non-temporal) formula over the given vars
- A, R, F are one- or two-state (non-temporal) formulas over the given vars
- all attributes are optional and their order is immaterial
- all attributes but the first three are repeatable

## System checking command

check-system	S
:input	$((i_1 \ \delta_1) \ \cdots \ (i_m \ \delta_m))$
:output	$( (o_1 \ \tau_1) \ \cdots \ (o_n \ \tau_n) )$
:local	$((s_1 \sigma_1) \cdots (s_p \delta_p))$
:assumption	(a A)
:reachable	(r R)
:fairness	(f F)
:current	(c C)
:query	$(q (g_1 \cdots g_q))$

- ; renaming of S's input vars
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- ; initiality condition
- ; trace query to be checked



Query *q* succeeds iff the formula below is *n*-satisfiable in LTL for some n > 0

 $I_S \wedge always T_S \wedge always A \wedge eventually R \wedge always eventually F$ 

where  $I_S$  and  $T_S$  are the initial state and transition predicate of S modulo the renamings above



Query q succeeds iff the formula below is satisfiable in LTL

 $I_S \wedge$  always  $T_S \wedge$  always  $A \wedge$  eventually  $R \wedge$  always eventually F

where  $I_S$  and  $T_S$  are the initial state and transition predicate of S modulo the renamings above



Query *q* succeeds iff the formula below is *n*-satisfiable in LTL for some n > 0

 $C \wedge$  always  $T_S \wedge$  always  $A \wedge$  eventually  $R \wedge$  always eventually F

where  $I_S$  and  $T_S$  are the initial state and transition predicate of S modulo the renamings above

S		
$(i_1 \ \delta_1) \ \cdots \ (i_m \ \delta_m))$	;	renaming of S's input vars
$(o_1 \tau_1) \cdots (o_n \tau_n))$	;	renaming of S's output vars
$((s_1 \sigma_1) \cdots (s_p \delta_p))$	;	renaming of S's local vars
(a A)	;	environmental assumption
(r R)	;	reachability condition
(f F)	;	fairness condition
(c C)	;	initiality condition
$(q (g_1 \cdots g_q))$	;	trace query to be checked
	$S \\ ((i_1 \ \delta_1) \ \cdots \ (i_m \ \delta_m)) \\ ((o_1 \ \tau_1) \ \cdots \ (o_n \ \tau_n)) \\ ((s_1 \ \sigma_1) \ \cdots \ (s_p \ \delta_p)) \\ (a \ A) \\ (r \ R) \\ (f \ F) \\ (c \ C) \\ (q \ (g_1 \ \cdots \ g_q)) \end{cases}$	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$

For each successful query, the model checker is expected to produce

- a  $\mathcal{T}$ -interpretation  $\mathcal{I}$  (of the free immutable symbols) and
- a *witnessing* trace in  $\mathcal{I}$

(check-system	S		
:input	$(i_1 \delta_1) \cdots (i_m \delta_m))$	;	renaming of S's input vars
:output	$(o_1 \tau_1) \cdots (o_n \tau_n))$	;	renaming of S's output vars
:local	$((s_1 \sigma_1) \cdots (s_p \delta_p))$	;	renaming of S's local vars
:assumption	(a A)	;	environmental assumption
:reachable	(r R)	;	reachability condition
:fairness	(f F)	;	fairness condition
:current	(c C)	;	initiality condition
:query	$(q (g_1 \cdots g_q))$	;	trace query to be checked

For each successful query, the model checker is expected to produce

- a  $\mathcal{T}$ -interpretation  $\mathcal{I}$  (of the free immutable symbols) and
- a witnessing trace in  $\mathcal{I}$

Different queries may be given different interpretations and traces

```
(check-system NonDetArbiter
 :input ((reg1 Bool) (reg2 Bool))
 :output ((ar1 Bool) (ar2 Bool))
 ; There are never concurrent requests
 :assumption (al (not (and reg1 reg2)))
 : The same request is never issued twice in a row
 :assumption (a2 (and (=> reg1 (not reg1'))
                       (=> rea2 (not rea2'))))
 ; Neg of: Every request is immediately granted
 :reachable (r (not (and (=> reg1 gr1) (=> reg2 gr2))))
 ; check the reachability of r under assumptions al and a2
 :query (q (a1 a2 r))
```

### Example 2 — Temporal queries

```
(define-system Historically :input ((b Bool)) :output ((b Bool))
 :init (= hb b) :trans (= hb' (and b' hb)))
(define-system Before :input ((b Bool)) :output ((bb Bool))
 :init (= bb' false) :trans (= bb' b))
(define-system Count :input ((b Bool)) :output ((c Int))
 :init (= c (ite b 1 0)) :trans (= c' (+ c (ite b 0 1))))
(define-system Monitor :input ((r1 Bool) (r2 Bool)) :output ((a1 Bool) (a2 Bool))
 :local ((a1 Bool) (a2 Bool) (b0 Bool) (b1 Bool) (b2 Bool)
         (h1 Bool) (h2 Bool) (c Int) (bf Bool))
 :subsvs (A (NonDetArbiter r1 r2 a1 a2))
 :subsys (H1 (Historically a1 h1))
 :subsys (H2 (Historically a2 h2))
 :subsvs (C (Count a1 c))
 :subsvs (B (Before b0 bf))
 :inv (and
   (= a1 (and (not r1) (not r2))) (= a2 (and (not g1) (not g2))) (= b0 (= c 4))
   (= b1 (=> h1 h2)); b1 = if there have been no requests, there have been no grants
   (= b2 (=> bf (not a1)))); b2 = a request is granted at most 4 times
(check-system Monitor :input ((r1 Bool) (r2 Bool))
 :output ((a1 Bool) (a2 Bool))
 :local (_ _ _ (b1 Bool) (b2 Bool) _ _ _ _)
 :assumption (A (not (and r1 r2))) :reachable (P (not (and b1 b2)))
 :query (0 (A P))
```

### Example 3 — Multiple queries

```
(check-system NonDetArbiter :input ((r1 Bool) (r2 Bool))
 :output ((a1 Bool) (a2 Bool))
  :assumption (a (not (and r1 r2)))
  : Neg of: Every request is (immediately) granted
  :reachable (p1 (not (and (=> r1 q1) (=> r2 q2))))
  ; Neg of: In the absence of other requests, every request is granted
  :reachable (p2 (not (=> (!= r1 r2) (and (=> r1 q1) (=> r2 q2)))))
  ; Neg of: A request is granted only if present
  :reachable (p3 (not (and (=> q1 r1) (=> q2 r2))))
  : Neg of: At most one request is granted at any one time
  :reachable (p4 (not (not (and g1 g2))))
  : Neg of: In case of concurrent requests, one of them is always granted
  :reachable (p5 (not (=> (and r1 r2) (or g1 g2))))
  :query (g1 (a p1)) :query (g2 (a p2)) :query (g3 (a p3))
  :query (q4 (a p4)) :query (q5 (a p5))
```

Each query can be witnessed by a different  $\mathcal{T}$ -interpretation and trace in it

### **Output format for check-system**

```
 (\text{define-system } A : \text{input } ((i \ \sigma_A)) : \text{output } ((o \ \tau_A)) : \text{local } ((s \ \theta_A)) \dots) \\ (\text{define-system } B : \text{input } ((i \ \sigma_B)) : \text{output } ((o \ \tau_B)) : \text{local } ((s \ \theta_B)) \\ : \text{subsys } ( \dots (S \ (A \dots)) \dots) \dots) \\ (\text{check-system } B \ \dots : \text{fairness } (f \ \dots) : \text{reachable } (r \ \dots) \dots \\ : \text{query } (q \ (r \ f \ \dots)) \dots )
```

Output:

### Special predicate: Deadlock

For every system  $S = (I_S[i, o, s], T_S[i, o, s, i', o', s'])$ Deadlock is a predicate (implicitly) over *i*, *o*, *s* 

> A state  $\{i \mapsto i_0, o \mapsto o_0, s \mapsto s_0\}$  satisfies Deadlock, or is deadlocked, iff it satisfies the formula  $\exists i' \forall o' \forall s' \neg T_s[i, o, s, i', o', s']$

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For every system  $S = (I_S[i, o, s], T_S[i, o, s, i', o', s'])$ Deadlock is a predicate (implicitly) over *i*, *o*, *s* 

### A state $\{i \mapsto i_0, o \mapsto o_0, s \mapsto s_0\}$ satisfies Deadlock, or *is deadlocked*,

#### iff

it satisfies the formula  $\exists i' \forall o' \forall s' \neg T_S[i, o, s, i', o', s']$ 

### Uses of **Deadlock**

- (check-system S ... :assumption (a A) :current (d Deadlock) :query (a d)) checks the existence of deadlocked states under assumption A
- (check-system S ···· :assumption (a A) :reachable (d Deadlock) :query (a d))
   checks the reachability of deadlocked states under assumption A
- (check-system S ···· :fairness (f true) :reachable (r R) :query (f r)) checks the reachability of R on infinite (hence deadlock-free) traces

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   checks the reachability of R on infinite (hence deadlock-free) traces

## What's intentionally missing (and why)

- Restrictions to just bit vector types
   Other types are useful.
- Stronger syntactic restrictions for **:init** and **:trans** formulas
- Direct support for LTL, or your favorite temporal logic, in **check-system**
- Global (mutable) variables *a la* SAL
- Parametric components as in SMV or SAL
- Compositional reasoning features (i.e., assume-guarantee contracts)
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#### Discussion

# What currently, intentionally or unintentionally, missing features would be imperative to have?

# **Possible Extensions**

## **Multiqueries**

(check-sys	tem S			
:input	$((i_1 \ \delta_1) \ \cdots \ (i_m \ \delta_m))$			
:output	$((o_1 \  au_1) \ \cdots \ (o_n \  au_n))$			
;queries	(( $q_1$ ( $g_{1,1}$ $\cdots$ $g_{1,n_1}$ )) $\cdots$	<b>(</b> <i>q</i> <sub><i>k</i></sub>	<b>(</b> <i>g</i> <sub><i>k</i>,1</sub>	 $g_{k,n_k}$ )))

- Each query  $q_i$  can be witnessed by a different trace
- However, each free immutable symbol has the same interpretation across all queries

## **Executable** system definitions

Local and output variables are defined exclusively equationally

```
(define-system TimedSwitch :input ((press Bool)) :output ((sig Bool))
 :local ((s LightStatus) (n Int))
 :inv-def (
   (sig (= s 0n))
 :init-def (
   (n 0)
   (s (ite press On Off))
 :next-def
   (s' (ite press' (ite (= s Off) On Off))
         (ite (= s Off) Off (ite (< n 10) On Off))))
   (n' (ite (or (= s Off) (s' Off)) 0 (+ n 1)))
 )
```

Restrictions: (guaranteeing progressiveness and executability)

- Each local or output variable must be listed in :inv-def or in both :init-def and :next-def
- No definitional cycles
- No uninterpreted symbols

## Parametric definitions – Part I

```
(define-system Delay :param ((V Type) (d V) (n Int)) :input ((in V))
  :output ((out V))
 :local ((a (Array Int V)))
  :inv (and
   (= in (select a 0))
   (= out (select a n))
  :init (forall ((i Int)) (=> (<= 1 i n)
           (= (select a i) d))
  :trans (forall ((i Int)) (=> (<= 1 i n))
            (= (select a' i) (select a (- i 1))))
(check-system Delay :param ((V String) (d "") (n 4)) :input ((in String))
 :output ((out String))
  :local ((a (Array Int String)))
```

Restrictions: parameters are immutable (rigid)

## Parametric definitions — Part II

#### New binders:

(foreach  $((i_1 \ l_1 \ h_1) \ \cdots \ (i_n \ l_n \ h_n)) \ F)$ 

(for some ( $(i_1 \ l_1 \ h_1) \ \cdots \ (i_n \ l_n \ h_n)$ ) F)

where

- *i*<sub>1</sub>, ..., *i*<sub>n</sub> are (integer) identifiers, the bound vars
- *l<sub>k</sub>* and *h<sub>k</sub>* are integer expressions that can eventually be evaluated statically
- *F* is a formula with free occurrences of *i*<sub>1</sub>, ..., *i*<sub>n</sub>

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(foreach  $((i_1 \ l_1 \ h_1) \ \cdots \ (i_n \ l_n \ h_n)) \ F)$ (forsome  $((i_1 \ l_1 \ h_1) \ \cdots \ (i_n \ l_n \ h_n)) \ F)$ 

where

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- *F* is a formula with free occurrences of *i*<sub>1</sub>, ..., *i*<sub>n</sub>

#### Semantics

 $\begin{array}{rcl} (\text{foreach } ((i \ l \ h)) \ F) &\equiv & (\text{and } F[l/i] \ F[(l+1)/i] \ \cdots \ F[l/i]) \\ (\text{forsome } ((i \ l \ h)) \ F) &\equiv & (\text{or } F[l/i] \ F[(l+1)/i] \ \cdots \ F[l/i]) \\ (\text{foreach } (b_1 \ \cdots \ b_n) \ F) &\equiv & (\text{foreach } (b_1) \ (\text{foreach } (b_2 \ \cdots \ b_n) \ F)) \\ (\text{forsome } (b_1 \ \cdots \ b_n) \ F) &\equiv & (\text{forsome } (b_1) \ (\text{forsome } (b_2 \ \cdots \ b_n) \ F)) \end{array}$ 

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- *F* is a formula with free occurrences of *i*<sub>1</sub>, ..., *i*<sub>n</sub>

#### Note

- (foreach ((*i* l h)) F)  $\equiv$  true when l > h
- (forsome ((i l h)) F)  $\equiv$  false when l > h
- (foreach ((*i* l h)) F)  $\equiv$   $F \equiv$  (forsome ((*i* l h)) F) when l = h

#### **Examples**

```
(define-system A :input ((i \tau)) :output ((o \tau)) ... )
```

```
; synchronous composition of A with itself n times
(define-system C :param ((n Int))
 :input ((i \tau))
 :output ((o \tau))
 :local ((s (Arrav Int \tau))
 :inv (and
   (= i \text{ (select } s 0))
   (= o (select s n))
   (foreach ((k \ 1 \ n)))
      (A \text{ (select } s (-k 1)) \text{ (select } s k)))
```